

Sample test case

We provide two traces and the output files for you to test your simulator. These traces will not be used in the final grading test. Each output file is generated using the follow command.

* trace2.pzip - Simulate 30 instructions:

```
./sim --print_inst=1 --max_inst_count=30 --max_sim_count=0 --print_pipe_freq=1 --  
trace_file="trace2.pzip" --output_file="sim2small.out" > dump2small.txt
```

output files:
dump2small.txt
sim2small.out

- Simulate the whole trace: ./sim --print_inst=0 --max_inst_count=0 --
max_sim_count=0 --print_pipe_freq=0 -- trace_file="trace2.pzip" --
output_file="sim2full.out" > dump2full.txt

output files:
dump2full.txt
sim2full.out

* trace3.pzip - Simulate the whole trace:

```
./sim --print_inst=0 --max_inst_count=0 --max_sim_count=0 --print_pipe_freq=0 --  
trace_file="trace3.pzip" --output_file="sim3full.out" > dump3full.txt
```

output files:
dump3full.txt
sim3full.out

Note that we will not check the latch value, so you only need to worry about the data/control hazard counter and cycles. In addition, we only grade base on IPC, so if the cycle count is off by several cycles it should be fine. The total instruction count may also be offset by 1, depending on if you count the last op or not.